AMENDMENTS TO THE CLAIMS

Please **AMEND** claim 1-4, 6-13, and 15-17 as shown below.

Please **CANCEL** claims 18 and 19, without prejudice or disclaimer to the subject matter contained therein.

Please **ADD** claim 20 as shown below.

The following is a complete list of all claims in this application.

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1. (Currently amended) A driving module for applying a driving signal to a display cell circuit ~~having a plurality of signal transmission lines and~~ formed on a transparent substrate ~~through the plurality of signal transmission lines~~, the driving module comprising:

a flexible board;

a driving circuit mounted on the flexible board;

a ~~plurality of driving signal input/output lines~~ driving signal line group which ~~are~~ is ~~electrically communicated~~ in electrical communication with the driving circuit and the display cell circuit so as to input/output the driving signal; and

~~an~~ inspecting ~~means~~ patterns formed on the ~~plurality of driving signal input/output lines~~ driving signal line group for inspecting states of the ~~plurality of driving signal input/output lines~~ driving signal line group and the driving signal.

2. (Currently amended) The driving module as claimed in claim 1, wherein the ~~plurality of driving signal input/output lines~~ driving signal line group ~~are~~ is formed on the flexible board disposed at a side of the transparent substrate.

3. (Currently amended) The driving module as claimed in claim 1, wherein the driving circuit is a gate driving circuit, and the driving signal is a gate driving signal which is applied from the gate driving circuit to a gate of the display cell circuit ~~through the signal transmission lines~~.

4. (Currently amended) The driving module as claimed in claim 3, wherein the ~~plurality of driving signal input/output lines comprised~~ driving signal line group comprises:

a plurality of gate driving signal input lines which are formed on the flexible board ~~for providing~~ to provide the gate driving signal to the gate driving circuit;

a plurality of gate driving signal bypass lines which are formed on the flexible board ~~for providing~~ and provide the gate driving signal supplied from the gate driving circuit to a next driving circuit; and

a plurality of gate driving signal output lines which ~~is~~ are connected between the gate driving circuit and the signal transmission lines ~~so as~~ to provide the gate driving signal supplied from the ~~plurality of gate driving signal input lines~~ gate driving circuit to the signal transmission lines.

5. (Original) The driving module as claimed in claim 4, wherein each of the plurality of gate driving signal input lines is correspondingly connected to each of the plurality of gate driving signal bypass lines in the gate driving circuit.

6. (Currently amended) The driving module as claimed in claim 5, wherein the inspecting ~~means is~~ patterns are formed at ~~only one group of~~ the plurality of gate driving signal input lines ~~and the plurality of gate driving signal bypass lines~~.

7. (Currently amended) The driving module as claimed in claim 5, wherein the inspecting ~~means is separately formed at the plurality of gate driving signal input lines and the plurality of gate driving signal bypass lines, and in a line in which a gate driving signal input line is electrically communicated with a gate driving signal bypass lines, the inspecting means is formed at only one of the gate driving signal input line and the gate driving signal bypass line~~ patterns include a first inspecting pattern formed at the gate driving signal input line, and a second inspection pattern formed at the gate driving signal bypass line, such that the first inspecting pattern is not electrically connected to the second inspecting pattern.

8. (Currently amended) The driving module as claimed in claim 3, wherein each of

the inspecting ~~means is formed by point shaped patterns having an area larger than an area~~ patterns has a width that is larger than a width of each gate driving signal input line and gate driving signal bypass line.

9. (Currently amended) A liquid crystal display device comprising:

a liquid crystal display panel having ~~a plurality of first and second signal transmission~~ gate lines and data lines and display cell circuits which are connected to ~~pairs of first and second signal transmission~~ the data lines and gate lines respectively, the liquid crystal display panel displaying an image in response to first and second driving signals inputted through the ~~first and second signal transmission~~ data lines and the gate lines;

an integrated printed circuit board ~~for generating~~ that generates the first and second driving signals;

a plurality of first driving modules which are electrically connected between the integrated printed circuit board and the ~~plurality of first signal transmission~~ data lines so as to transmit the first driving signal to the ~~first signal transmission~~ data lines ~~after controlling a time for applying the first driving signal from the integrated printed circuit board~~; and

a plurality of second driving modules having a plurality of ~~driving signal input/output lines~~ driving signal line groups that are electrically connected to the ~~plurality of second signal transmission~~ gate lines, the second driving modules are electrically connected to the integrated printed circuit board through signal transmission lines formed on liquid crystal display panel, and the second driving modules transmitting the second driving signal to the ~~second signal transmission~~ gate lines ~~after controlling the time for applying the second driving signal from the integrated printed circuit board, the second driving modules inspecting states of the second driving signal and the plurality of driving signal input/output lines~~.

10. (Currently amended) The liquid crystal display device as claimed in claim 9, wherein the plurality of ~~driving signal input/output lines~~ driving signal line groups are formed on the flexible board disposed at a side of the liquid crystal display panel.

11. (Currently amended) The liquid crystal display device as claimed in claim 9, wherein

~~the first signal transmission lines are data signal transmission lines, the second signal transmission lines are gate signal transmission lines,~~ the first and second driving signals are data and gate driving signals, respectively, and the first and second driving modules are data and gate driving modules, respectively.

12. (Currently amended) The liquid crystal display device as claimed in claim 11, wherein the gate driving module comprises:

a flexible board;

a gate driving circuit mounted on the flexible board;

a plurality of ~~driving signal input/output lines~~ driving signal line groups ~~which are electrically communicated~~ in electrical communication with the gate driving circuit and the display cell circuit so as to input/output the gate driving signal; and

~~an~~ inspecting ~~means~~ patterns formed on the plurality of ~~driving signal input/output lines~~ driving signal line groups for inspecting states of the plurality of ~~driving signal input/output lines~~ driving signal line groups and the driving signal.

13. (Currently amended) The liquid crystal display device as claimed in claim 12, wherein the plurality of ~~driving signal input/output lines~~ driving signal line group comprises:

a plurality of gate driving signal input lines which are formed on the flexible board ~~for providing~~ and provide the gate driving signal to the gate driving circuit;

a plurality of gate driving signal bypass lines which are formed on the flexible board ~~for providing~~ and provide the gate driving signal supplied from the gate driving circuit to a next gate driving circuit; and

a plurality of gate driving signal output line which ~~is~~ are connected between the gate driving circuit and the ~~second signal transmission~~ gate lines so as to provide the gate driving signal supplied from the ~~plurality of gate driving signal input lines~~ gate driving circuit to the ~~signal transmission~~ gate lines.

14. (Original) The liquid crystal display device as claimed in claim 13, wherein each of the plurality of gate driving signal input lines is correspondingly connected to each of the

plurality of gate driving signal bypass lines in the gate driving circuit.

15. (Currently Amended) The liquid crystal display device as claimed of claim 14, wherein the inspecting ~~means is~~ patterns are formed at ~~only one group of~~ the plurality of gate driving signal input lines~~, and the plurality of gate driving signal bypass lines.~~

16. (Currently Amended) The liquid crystal display device of claim 14, wherein the inspecting ~~means is separately formed at the plurality of gate driving signal input lines and the plurality of gate driving signal bypass lines, and in a line in which a gate driving signal input line is electrically communicated with a gate driving signal bypass line, the inspecting means is formed at only one of the gate driving signal input line and the gate driving signal bypass line.~~ patterns include a first inspecting pattern formed at the gate driving signal input line, and a second inspecting pattern formed at the gate driving signal bypass line, such that the first inspecting pattern is not electrically connected to the second inspecting pattern.

17. (Currently Amended) The liquid crystal display device as claimed in claim 16, wherein the each of the inspecting ~~means is formed by point shaped patterns having an area larger than an area~~ patterns has a width that is larger than a width of each gate driving signal input line and gate driving signal bypass line.

18. (Canceled)

19. (Canceled)

20. (New) A display apparatus, comprising:

a transistor substrate;

an integrated printed circuit board arranged adjacent to the transistor substrate;

a first driving module having a first end and a second end, the first end connected to the integrated printed circuit board and the second end connected to the transistor substrate, wherein the driving module includes a plurality of signal transmission lines; and

a second driving module having a first end and a second end, the first end connected to the transistor substrate, whenever the driving module comprises a plurality of input signal lines in electrical communication with the plurality of signal transmission lines, and a portion of the plurality of input signal lines include an inspecting pattern to allow for an inspection of an electrical signal in the plurality of input signal lines.